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Reduced-order Structure-preserving Model for Parallel-connected Three-phase Grid-tied Inverters

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Abstract—Next-generation power networks will contain large numbers of grid-connected inverters satisfying a significant fraction of system load. Since each inverter model has a relatively large number of dynamic states, it is impractical to analyze complex system models where the full dynamics of each inverter are retained. To address this challenge, we derive a reduced-order structure-preserving model for parallel-connected grid-tied three-phase inverters. Here, each inverter in the system is assumed to have a full-bridge topology, *LCL* filter at the point of common coupling, and the control architecture for each inverter includes a current controller, a power controller, and a phase-locked loop for grid synchronization. We outline a structure-preserving reduced-order inverter model with lumped parameters for the setting where the parallel inverters are each designed such that the filter components and controller gains scale linearly with the power rating. By structure preserving, we mean that the reduced-order three-phase inverter model is also composed of an *LCL* filter, a power controller, current controller, and PLL. We show that the system of parallel inverters can be modeled exactly as one aggregated inverter unit and this equivalent model has the same number of dynamical states as any individual inverter in the system. Numerical simulations validate the reduced-order model.

I. INTRODUCTION

Distribution networks are witnessing an increased integration of power-electronics inverters serving as grid interfaces to renewable resources, electrical vehicles, and loads. For instance, today there are roughly 800,000 Enphase microinverters on the Hawaiian island of Oahu alone [1] and this number will only grow as Hawaii aims to meet its goal of obtaining 100% of its energy from renewable sources by 2045 [2]. To aid analysis and control, it is critical to develop computationally affordable models that scale with penetration level and accurately capture pertinent dynamics of power electronics inverters. This paper takes a step in this direction by formulating a reduced-order model for a collection of parallel-connected grid-tied three-phase inverters as may be seen in photovoltaic energy conversion systems, electric-vehicle charging stations, and railway auxiliary power supplies [3]–[5].

Without loss of generality, we examine a three-phase inverter with a full-bridge topology and output *LCL* filter. As

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shown in Fig. 1a, the inverter control system includes: a current controller (that generates the PWM reference signals), a power controller (that responds to active- and reactive-power setpoints and generates current references for the current controller), and a phase-locked loop (PLL) (for grid synchronization) [6]. Models of this sort generally have more than 10 dynamical states (the particular one we study has 15); and therefore, it is computationally infeasible and analytically unwieldy to study large collections of such inverters with different power ratings. To address these limitations, we derive a structure-preserving reduced-order inverter model for the setting where individual inverters in the parallel setup have the same topology with filter components and controller gains that scale linearly with the power rating. By structure preserving, we mean that the reduced-order model itself is a three-phase inverter that is also composed of an *LCL* filter, a power controller, current controller, and PLL, i.e., it has the same structure and the same number of dynamical states as any individual inverter in the parallel multi-inverter system.

Model-reduction methods for individual inverters and synchronous machines have received attention in the literature [7]–[9]. While model-reduction methods to analyze the collective dynamics of machines and droop-controlled islanded inverters have received attention [10], [11], this work focuses on large collections of the most ubiquitous type of inverter installed on systems today—grid-tied inverters with conventional grid-following current controllers and PLLs. We anticipate the models developed here to be applicable in such problems as the study of dynamic interactions between machines and inverters, as well as stability analysis of networks with high inverter penetration [12]–[14].

The remainder of this paper is organized as follows. In Section II, we introduce a three-phase grid-connected inverter model and power scaling laws for the inverter. In Section III, we describe how the states of the inverter are scaled based on its power-scaling parameter, and propose the reduced-order structure-preserving model for parallel-connected inverters. To validate the proposed reduced-order model, we compare numerical simulation of a parallel system to its corresponding reduced-order inverter in Section IV. Finally, we conclude this paper and outline a few pertinent directions for future work in Section V.

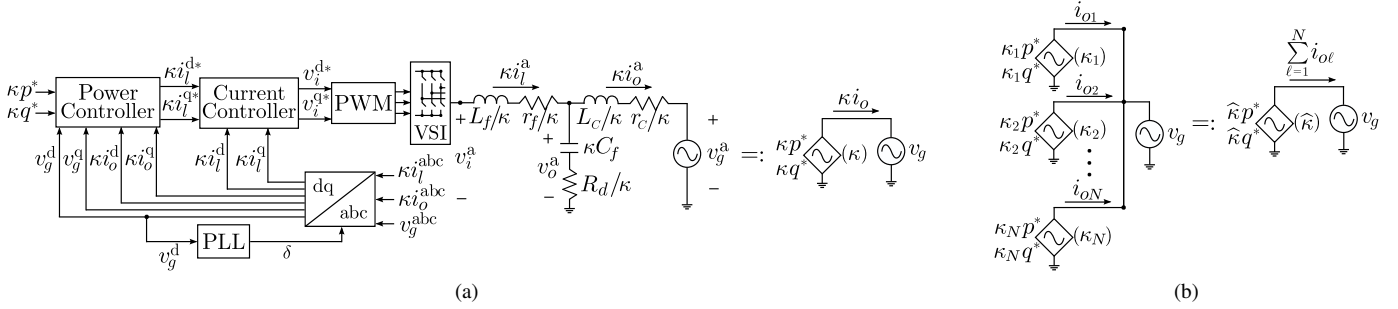


Fig. 1: (a) Block diagram of the three phase inverter (one leg of the LCL filter is depicted) and adopted shorthand. (b) For the parallel connection of N inverters we obtain the power-scaling parameter, $\hat{\kappa}$, for a reduced-order structure-preserving model.

Notation: The matrix transpose will be denoted by $(\cdot)^T$. The spaces of $N \times 1$ real-valued vectors is denoted by \mathbb{R}^N ; and $\mathbb{R}^{N \times N}$ denotes the space of $N \times N$ real-valued matrices. A diagonal matrix formed with diagonal entries composed of entries of the vector x is denoted by $\text{diag}(x)$; $\mathbb{1}_n$ and $\mathbb{0}_n$ denote an all ones and all zeros vectors of length n , respectively; I_n denotes an n -by- n identity matrix; $\mathbb{0}_{m \times n}$ denotes an all zeros matrix of size m -by- n .

II. THE INVERTER MODEL AND POWER SCALING LAWS

A block diagram of the three-phase inverter is illustrated in Figure 1a. We assume a voltage source inverter (VSI) with an H-bridge topology and an output LCL filter (L_f , C_f , L_c). The grid voltage and frequency are denoted by v_g and ω_g , respectively. The control architecture for each inverter includes: an inner-loop current controller, an outer-loop power controller, and a phase-locked loop (for grid synchronization). As shown in Fig. 1b, we are primarily interested in the collective behavior of N such inverters connected in parallel to the grid. We begin this section with an overview on how the inverters are designed for different power ratings, the features of the reduced-order model sought for the parallel collection of inverters, and then briefly discuss the controller and LCL filter dynamics. Lastly, we represent the dynamics of the inverter in state-space form.

A. Scaling Individual Inverters with Power Rating

We first introduce the notion of a *power-scaling parameter*, κ , which is defined as

$$\kappa := \frac{p_{\text{rated}}}{p_{\text{base}}}, \quad (1)$$

where p_{rated} is the rated power of a given inverter, and p_{base} is a system-wide base value. We make the assumption that both real and reactive power ratings scale linearly with κ . In the remainder of the digest, we will denote the base active- and reactive-power setpoints as p^* and q^* . Notice from Fig. 1a that the reference-power setpoints for the inverter are given by κp^* , κq^* . Therefore, the output active and reactive power injected by each inverter into the grid are directly proportional to κ . We scale elements of the LCL filter in the manner shown in Fig. 1a so that the output current is inversely proportional

to the impedance of the filter. With regard to the controllers, we also scale the gains of the current controller by $1/\kappa$ so that its outputs, i.e., the reference for the input voltage of the LCL filter, do not depend on κ , and neither does the voltage drop across the LCL filter. Thus, the output current, and therefore the output power, of the inverter scales directly proportionally to κ .

The scaling approach described above is admittedly assumptive by nature. However, it is a herculean—if not impossible—task to ascertain how commercial inverter manufacturers would scale the cyber-physical architectures of inverters with power rating. Therefore, we base our analysis around this scaling approach, taking solace in the fact that it is grounded in and guided by some fundamental engineering insights.

B. Desired Features of the Structure-preserving Reduced-order Model

With the control and physical architecture of individual inverters highlighted in Fig. 1a, and the procedure to scale the inverter design to accommodate different power ratings discussed above, we bring attention to Fig. 1b to describe the main goal of this work. We consider a collection of N inverters with different power ratings (described by power-scaling parameters $\kappa_1, \dots, \kappa_N$) connected in parallel to the grid bus. The dynamics of each inverter include those of the different control blocks and LCL filters illustrated in Fig. 1a, and are described collectively by a 15-order model (which will be spelled out subsequently). We derive a structure-preserving reduced-order model for this parallel connection. Particularly, we will show that an inverter model with power-scaling parameter, $\hat{\kappa} = \sum_{\ell=1}^N \kappa_{\ell}$ perfectly captures the input-output behavior of the N -inverter collection with a dynamical-system model that has the same order and structure as any individual inverter.

C. Model of an Individual Inverter

With the design procedure adopted for scaling inverters in place, and the goal of this effort described, we next briefly overview individual portions of the dynamical models for the individual inverters in Fig. 1a. We start with the reference-frame transformation, and then go through the phase-locked loop (PLL), the current controller, and the power controller. In

each case, we describe the model assuming assuming $\kappa = 1$, but indicate how the dynamics are modified for an inverter with a power rating that is not the base value.

1) *Reference-frame transformation*: Sinusoidally varying three-phase signals (x^a, x^b, x^c) in balanced settings are co-ordinate transformed to equivalent DC signals (x^d, x^q) using Park's transformation:

$$\begin{aligned} \begin{bmatrix} x^d \\ x^q \end{bmatrix} &= \frac{2}{3} \begin{bmatrix} \cos(\delta) & \cos(\delta - \frac{2\pi}{3}) & \cos(\delta + \frac{2\pi}{3}) \\ -\sin(\delta) & -\sin(\delta - \frac{2\pi}{3}) & -\sin(\delta + \frac{2\pi}{3}) \end{bmatrix} \begin{bmatrix} x^a \\ x^b \\ x^c \end{bmatrix} \\ &=: \frac{2}{3} \Psi \begin{bmatrix} x^a \\ x^b \\ x^c \end{bmatrix}, \end{aligned} \quad (2)$$

where δ is the instantaneous angle generated by the PLL. The change in coordinates is signified with the abc -dq block in Fig. 1a. We further note that $[x^a, x^b, x^c]^T = \Psi^T [x^d, x^q]^T$. To illustrate the adopted notation in abc and dq coordinates, consider the grid voltage, v_g , without loss of generality. First, $v_g^{abc} := [v_g^a, v_g^b, v_g^c]^T$ captures v_g in abc coordinates. In the dq reference frame, we define $v_g^{dq} := [v_g^d, v_g^q]^T$, and $v_g^{abc} = \Psi^T v_g^{dq}$.

2) *Phase-locked Loop*: The PLL is in feedback with the dq transformation for the grid voltage, and it modulates the angle δ such that $v_g^d \rightarrow 0$ asymptotically. (Elementary trigonometric identities coupled with (2) illustrate that when $v_g^d = 0$, δ is the instantaneous phase angle of v_g^a , i.e., the inverter is *phase locked* with the grid.) It consists of a low pass filter (with cut-off frequency $\omega_{c,PLL}$) and a PI controller (with PI gains k_{PLL}^p and k_{PLL}^i). The dynamics of the PLL that generate the angle δ are given by

$$\begin{aligned} \dot{v}_{PLL} &= \omega_{c,PLL}(v_g^d - v_{PLL}), \\ \dot{\phi}_{PLL} &= -v_{PLL}, \\ \dot{\delta} &= 2\pi \times 60 - k_{PLL}^p v_{PLL} + k_{PLL}^i \phi_{PLL} =: \omega_{PLL}. \end{aligned} \quad (3)$$

From the dynamics, we can see that at steady state, $v_g^d = v_{PLL} = 0$. Furthermore, when the grid frequency, $\omega_g = 2\pi \times 60$ [rad · sec⁻¹], it follows that $\dot{\delta} = \omega_{PLL} = 2\pi \times 60$ [rad · sec⁻¹].

Remark 1 (Dynamics of PLL in scaled inverter). The same dynamics in (3) are utilized for inverters with different power ratings, i.e., for the case $\kappa \neq 1$.

3) *Output LCL Filter*: The dynamics of the LCL filter (in the dq reference frame) are derived by running pertinent time-domain circuit equations through the dq transformation with angle δ . This yields

$$\begin{aligned} \dot{i}_l^{dq} &= \frac{1}{L_f} (-r_f i_l^{dq} + v_i^{dq} - v_o^{dq}) + \begin{bmatrix} 0 & 1 \\ -1 & 0 \end{bmatrix} \omega_{PLL} i_l^{dq}, \\ \dot{i}_o^{dq} &= \frac{1}{L_c} (-r_c i_o^{dq} + v_o^{dq} - v_g^{dq}) + \begin{bmatrix} 0 & 1 \\ -1 & 0 \end{bmatrix} \omega_{PLL} i_o^{dq}, \\ \dot{v}_o^{dq} &= R_d (i_l^{dq} - i_o^{dq}) - \begin{bmatrix} 0 & 1 \\ -1 & 0 \end{bmatrix} \omega_{PLL} R_d (i_l^{dq} - i_o^{dq}) \\ &\quad + \frac{1}{C_f} (i_l^{dq} - i_o^{dq}) + \begin{bmatrix} 0 & 1 \\ -1 & 0 \end{bmatrix} \omega_{PLL} v_o^{dq}, \end{aligned} \quad (4)$$

where $i_l^{dq} = [i_l^d, i_l^q]^T$, $i_o^{dq} = [i_o^d, i_o^q]^T$, $v_i^{dq} = [v_i^d, v_i^q]^T$, and $v_o^{dq} = [v_o^d, v_o^q]^T$.

Remark 2 (Dynamics of LCL filter in scaled inverter). While the dynamics above correspond to the case where the power-scaling parameter, $\kappa = 1$; for inverters with power ratings that are scaled values of this base setting, we utilize the filter parameters L_f/κ , r_f/κ , κC_f , R_d/κ , L_c/κ , r_c/κ with κ chosen according to (1).

4) *Power Controller*: The power controller consists of low pass filters and PI controllers, with its outputs to be the reference for the current controller:

$$\begin{aligned} i_l^{d*} &= k_Q^p (q^* - q_{avg}) + k_Q^i \int q^* - q_{avg}, \\ i_l^{q*} &= k_P^p (p^* - p_{avg}) + k_P^i \int p^* - p_{avg}, \end{aligned}$$

where p^*, q^* are reference active- and reactive-power setpoints. Furthermore, p_{avg} and q_{avg} are low-pass-filtered versions of the inverter output active and reactive power outputs:

$$\dot{p}_{avg} = \omega_c (p - p_{avg}), \quad \dot{q}_{avg} = \omega_c (q - q_{avg}), \quad (5)$$

where p and q are the instantaneous active and reactive output power (measured at the grid terminals) and given by

$$p = \frac{3}{2} (v_g^d i_o^d + v_g^q i_o^q), \quad q = \frac{3}{2} (v_g^q i_o^d - v_g^d i_o^q).$$

To ease notation and exposition in subsequent developments, we will find it useful to define:

$$\dot{\phi}_p := p^* - p_{avg}, \quad \dot{\phi}_q := q^* - q_{avg}. \quad (6)$$

Remark 3 (Dynamics of power controller in scaled inverter). For inverters with $\kappa \neq 1$, the power setpoints as scaled as κp^* and κq^* ; all other dynamics reported above are retained.

5) *Current Controller*: The current controller consists of two PI controllers and feedforward terms, with its outputs to be the reference for the terminal voltage v_i :

$$\begin{aligned} v_i^{d*} &= -\omega_{PLL} L_f i_l^q + k_{id}^p (i_l^{d*} - i_l^d) + k_{id}^i \int i_l^{d*} - i_l^d, \\ v_i^{q*} &= \omega_{PLL} L_f i_l^d + k_{iq}^p (i_l^{q*} - i_l^q) + k_{iq}^i \int i_l^{q*} - i_l^q. \end{aligned}$$

To ease notation and exposition in subsequent developments, we will find it useful to define:

$$\dot{\gamma}^d = i_l^{d*} - i_l^d, \quad \dot{\gamma}^q = i_l^{q*} - i_l^q. \quad (7)$$

The (three-phase) PWM modulation signals for the inverter are then obtained as $v_i^{abc*} = \Psi^T v_i^{dq*}$. With reference to Fig. 1a, for an ideal inverter we have that the inverter terminal voltage, $v_i^{abc} = v_i^{abc*}$ (and equivalently $v_i^{dq} = v_i^{dq*}$).

Remark 4 (Dynamics of current controller in scaled inverter). While the dynamics above correspond to the case where the power-scaling parameter, $\kappa = 1$; for inverters with power ratings that are scaled values of this base setting, we utilize the parameters k_{id}^p/κ , k_{iq}^p/κ , k_{id}^i/κ , k_{iq}^i/κ .

D. State-space Model for the Inverter Dynamics

For the model in (3)–(7), we assumed that $\kappa = 1$ and hence, it defines the dynamics in the *unscaled* inverter. For this case, the controller and LCL filter dynamics can be compactly represented in state-space form as follows

$$\dot{\underline{x}} = A\underline{x} + B\underline{u}_1 + g(\underline{x}, \underline{u}_2), \quad (8)$$

where the states are $\underline{x} = [i_l^d, i_l^q, i_o^d, i_o^q, \gamma^d, \gamma^q, p_{\text{avg}}, q_{\text{avg}}, \phi_p, \phi_q, v_o^d, v_o^q, v_{\text{PLL}}, \phi_{\text{PPL}}, \delta]^T$, and the inputs include $\underline{u}_1 = [p^*, q^*]^T$ and $\underline{u}_2 = [v_g^a, v_g^b, v_g^c]^T = v_g^{\text{abc}}$. Here, the matrices $A \in \mathbb{R}^{15 \times 15}$, $B \in \mathbb{R}^{15 \times 2}$, and function $g : \mathbb{R}^{15} \times \mathbb{R}^3 \rightarrow \mathbb{R}^{15}$ can be derived from the dynamical-system models in (6)–(7) straightforwardly. For completeness, their entries are listed in the Appendix.

Now, let us consider a *scaled* inverter with $\kappa \neq 1$. If we replace the unscaled model parameters $L_f, r_f, C_f, R_d, L_c, r_c, k_{id}^p, k_{iq}^p, k_{id}^i, k_{iq}^i$ with $\kappa^{-1}L_f, \kappa^{-1}r_f, \kappa C_f, \kappa^{-1}R_d, \kappa^{-1}L_c, \kappa^{-1}r_c, \kappa^{-1}k_{id}^p, \kappa^{-1}k_{iq}^p, \kappa^{-1}k_{id}^i, \kappa^{-1}k_{iq}^i$ (see Remark 1–4), we obtain the *scaled* inverter model

$$\dot{\underline{x}}^s = A^s \underline{x}^s + B^s \underline{u}_1^s + g^s(\underline{x}^s, \underline{u}_2^s), \quad (9)$$

where the inputs are $\underline{u}_1^s = \kappa \underline{u}_1$ and $\underline{u}_2^s = \underline{u}_2 = v_g^{\text{abc}}$. The matrices $A^s \in \mathbb{R}^{15 \times 15}$, $B^s \in \mathbb{R}^{15 \times 2}$, and function $g^s : \mathbb{R}^{15} \times \mathbb{R}^3 \rightarrow \mathbb{R}^{15}$ have the same structure as A , B , and g for the unscaled model albeit with parametric scalings given above. Next, define $\underline{\kappa} := [\kappa \mathbb{1}_{10}^T, \mathbb{1}_5^T]^T$, where $\mathbb{1}_\ell$ denotes an ℓ -length vector of all ones.

III. INVERTER SCALING AND REDUCED-ORDER MODEL

In this section, we begin with establishing the relationship between the states in the scaled and unscaled inverter models, then we propose a method of aggregating N parallel-connected inverters illustrated in Fig. 1b.

A. Scaling of Inverter States

Here, we establish the connection between the dynamics of the scaled and unscaled inverters.

Proposition 1. Consider the dynamics of the unscaled inverter (with $\kappa = 1$) and scaled inverter (with $\kappa \neq 1$) in (8) and (9), respectively. Suppose the initial conditions for the two dynamical systems at some time $t_0 \geq 0$ are such that $\underline{x}^s(t_0) = \text{diag}(\underline{\kappa})\underline{x}(t_0)$. If $\underline{u}_1^s = \kappa \underline{u}_1$ and $\underline{u}_2^s = \underline{u}_2$, it follows that $\underline{x}^s(t) = \text{diag}(\underline{\kappa})\underline{x}(t)$, $\forall t \geq t_0$.

Proof. We partition \underline{x} as $[\underline{x}_1^T, \underline{x}_2^T]^T$, where $\underline{x}_1^T = [i_l^d, i_l^q, i_o^d, i_o^q, \gamma^d, \gamma^q, p_{\text{avg}}, q_{\text{avg}}, \phi_p, \phi_q]^T$ and $\underline{x}_2^T = [v_o^d, v_o^q, v_{\text{PLL}}, \phi_{\text{PPL}}, \delta]^T$, and we also partition \underline{x}^s the same way. Then we write (8) and (9) as follows

$$\begin{bmatrix} \dot{\underline{x}}_1 \\ \dot{\underline{x}}_2 \end{bmatrix} = \begin{bmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{bmatrix} \begin{bmatrix} \underline{x}_1 \\ \underline{x}_2 \end{bmatrix} + \begin{bmatrix} B_1 \\ B_2 \end{bmatrix} \underline{u}_1 + g(\underline{x}, \underline{u}_2), \quad (10)$$

$$\begin{bmatrix} \dot{\underline{x}}_1^s \\ \dot{\underline{x}}_2^s \end{bmatrix} = \begin{bmatrix} A_{11}^s & A_{12}^s \\ A_{21}^s & A_{22}^s \end{bmatrix} \begin{bmatrix} \underline{x}_1^s \\ \underline{x}_2^s \end{bmatrix} + \begin{bmatrix} B_1^s \\ B_2^s \end{bmatrix} \underline{u}_1^s + g^s(\underline{x}^s, \underline{u}_2^s). \quad (11)$$

By observing the entries of the state-space matrices, it is straightforward to see the following relationships:

$$A_{11}^s = A_{11}, A_{12}^s = \kappa A_{12}, A_{21}^s = \frac{1}{\kappa} A_{21}, A_{22}^s = A_{22},$$

$$B_1^s = B_1, B_2^s = \frac{1}{\kappa} B_2.$$

Then we have

$$\text{diag}(\underline{\kappa})A = \begin{bmatrix} \kappa A_{11} & \kappa A_{12} \\ A_{21} & A_{22} \end{bmatrix} = \begin{bmatrix} \kappa A_{11}^s & A_{12}^s \\ \kappa A_{21}^s & A_{22}^s \end{bmatrix} = A^s \text{diag}(\underline{\kappa}), \quad (12)$$

$$\text{diag}(\underline{\kappa})B = \begin{bmatrix} \kappa B_1 \\ B_2 \end{bmatrix} = \begin{bmatrix} \kappa B_1^s \\ \kappa B_2^s \end{bmatrix} = B^s(\kappa \mathbb{I}_2). \quad (13)$$

Next, we are going to show that $g^s(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) = \text{diag}(\underline{\kappa})g(\underline{x}, \underline{u}_2)$. Notice that the PLL dynamics for both inverters are decoupled from the rest of internal states and their parameters are the same, so we can conclude that $v_g^{\beta s} = v_g^\beta, v_{\text{PLL}}^s = v_{\text{PLL}}, \phi_{\text{PPL}}^s = \phi_{\text{PPL}}, \delta^s = \delta$. Then, the following identities hold for v_g^d and v_g^q :

$$v_g^d(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) = \frac{2}{3} \left(\cos(\delta)v_g^a + \cos(\delta - \frac{2\pi}{3})v_g^b + \cos(\delta + \frac{2\pi}{3})v_g^c \right) = v_g^d(\underline{x}, \underline{u}_2) = v_g^d(\underline{x}, \underline{u}_2),$$

$$v_g^q(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) = \frac{2}{3} \left(\sin(\delta)v_g^a + \sin(\delta - \frac{2\pi}{3})v_g^b + \sin(\delta + \frac{2\pi}{3})v_g^c \right) = v_g^q(\underline{x}, \underline{u}_2) = v_g^q(\underline{x}, \underline{u}_2).$$

The nonzero entries of $g^s(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s)$ are given by

$$g_3^s(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) = (-k_{\text{PLL}}^p v_{\text{PLL}} + k_{\text{PLL}}^i \phi_{\text{PLL}}) \kappa i_o^q - \frac{\kappa}{L_c} v_g^d(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) = \kappa g_3(\underline{x}, \underline{u}_2^s),$$

$$g_4^s(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) = (k_{\text{PLL}}^p v_{\text{PLL}} - k_{\text{PLL}}^i \phi_{\text{PLL}}) \kappa i_o^d - \frac{\kappa}{L_c} v_g^q(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) = \kappa g_4(\underline{x}, \underline{u}_2^s),$$

$$g_7^s(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) = \frac{3}{2} \kappa \omega_c (v_g^d(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) i_o^d + v_g^q(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) i_o^q) = \kappa g_7(\underline{x}, \underline{u}_2^s),$$

$$g_8^s(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) = \frac{3}{2} \kappa \omega_c (v_g^q(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) i_o^d - v_g^d(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) i_o^q) = \kappa g_8(\underline{x}, \underline{u}_2^s),$$

$$g_{11}^s(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) = (-k_{\text{PLL}}^p v_{\text{PLL}} + k_{\text{PLL}}^i \phi_{\text{PLL}}) (-R_d i_l^q + v_o^q) + \frac{R_d}{L_c} v_g^d(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) = g_{11}(\underline{x}, \underline{u}_2^s),$$

$$g_{12}^s(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) = (k_{\text{PLL}}^p v_{\text{PLL}} - k_{\text{PLL}}^i \phi_{\text{PLL}}) (-R_d i_l^d + v_o^d) + \frac{R_d}{L_c} v_g^q(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) = g_{12}(\underline{x}, \underline{u}_2^s),$$

$$g_{13}^s(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) = \omega_{c, \text{PLL}} v_g^d(\underline{x}, \underline{u}_2) = g_{13}(\underline{x}, \underline{u}_2^s),$$

$$g_{15}^s(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) = 2\pi \times 60 = g_{15}(\underline{x}, \underline{u}_2^s, \underline{u}_2).$$

Therefore, we have

$$\text{diag}(\underline{\kappa})g(\underline{x}, \underline{u}_2) = g^s(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s). \quad (14)$$

Let us define function $h(\underline{x}^s, \underline{u}_2^s) : \mathbb{R}^{15} \times \mathbb{R}^2 \rightarrow \mathbb{R}^{15}$ to have the same structure as $g^s(\underline{x}^s, \underline{u}_2^s)$ except the following entries:

$$\begin{aligned} h_3(\underline{x}^s, \underline{u}_2^s) &= (-k_{\text{PLL}}^p v_{\text{PLL}}^s + k_{\text{PLL}}^i \phi_{\text{PLL}}^s) i_o^{\text{qs}}, \\ h_4(\underline{x}^s, \underline{u}_2^s) &= (k_{\text{PLL}}^p v_{\text{PLL}}^s - k_{\text{PLL}}^i \phi_{\text{PLL}}^s) i_o^{\text{ds}}, \\ h_{11}(\underline{x}^s, \underline{u}_2^s) &= (-k_{\text{PLL}}^p v_{\text{PLL}}^s + k_{\text{PLL}}^i \phi_{\text{PLL}}^s) \left(-\frac{R_d}{\kappa} i_l^{\text{qs}} + v_o^{\text{qs}} \right), \\ h_{12}(\underline{x}^s, \underline{u}_2^s) &= (k_{\text{PLL}}^p v_{\text{PLL}}^s - k_{\text{PLL}}^i \phi_{\text{PLL}}^s) \left(-\frac{R_d}{\kappa} i_l^{\text{ds}} + v_o^{\text{ds}} \right), \\ h_{13}(\underline{x}^s, \underline{u}_2^s) &= 0, \\ h_{15}(\underline{x}^s, \underline{u}_2^s) &= 0. \end{aligned}$$

Then, the following identity holds

$$g^s(\underline{x}^s, \underline{u}_2^s) - g^s(\text{diag}(\underline{\kappa})\underline{x}, \underline{u}_2^s) = h((\underline{x}^s - \text{diag}(\underline{\kappa})\underline{x}), \underline{u}_2^s). \quad (15)$$

Let us define $\underline{z} := \underline{x}^s - \text{diag}(\underline{\kappa})\underline{x}$, and note from (10) and (11) that

$$\begin{aligned} \dot{\underline{z}} &= \dot{\underline{x}}^s - \text{diag}(\underline{\kappa})\dot{\underline{x}} = A^s \underline{x}^s + B^s \underline{u}_1^s + g^s(\underline{x}^s, \underline{u}_2^s) \\ &\quad - \text{diag}(\underline{\kappa})A\underline{x} - \text{diag}(\underline{\kappa})B\underline{u}_1 - \text{diag}(\underline{\kappa})g(\underline{x}, \underline{u}_2). \end{aligned} \quad (16)$$

Leveraging identities (12)–(15), we can rewrite (16) as

$$\begin{aligned} \dot{\underline{z}} &= A^s(\underline{x}^s - \text{diag}(\underline{\kappa})\underline{x}) + h((\underline{x}^s - \text{diag}(\underline{\kappa})\underline{x}), \underline{u}_2^s) \\ &= A^s \underline{z} + h(\underline{z}, \underline{u}_2^s). \end{aligned} \quad (17)$$

It is straightforward to see $h(\mathbf{0}_{15}, \underline{u}_2^s) = \mathbf{0}_{15}$. Then, if we initialize $\underline{z}(t_0) = \underline{x}^s(t_0) - \text{diag}(\underline{\kappa})\underline{x}(t_0) = \mathbf{0}_{15}$, we have $\underline{z}(t) = \mathbf{0}_{15}, \forall t \geq t_0$. Therefore we have $\underline{x}^s(t) = \text{diag}(\underline{\kappa})\underline{x}(t), \forall t \geq t_0$. \square

B. Aggregation of Inverters

Consider the parallel-connection of N inverters with power-scaling factors $\kappa_1, \dots, \kappa_N$ illustrated in Fig. 1b. Define the equivalent power-scaling factor as $\hat{\kappa} := \sum_{\ell=1}^N \kappa_\ell$. For this system, define the reduced-order model

$$\dot{\underline{x}}^r = A^r \underline{x}^r + B^r \underline{u}_1^r + g^r(\underline{x}^r, \underline{u}_2^r), \quad (18)$$

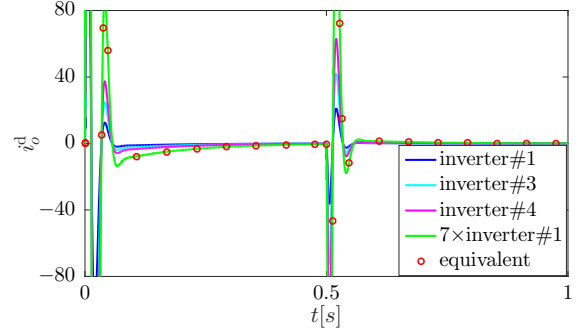
where the inputs \underline{u}_1^r and \underline{u}_2^r are given by $\underline{u}_1^r = \hat{\kappa}[p^*, q^*]^T$, $\underline{u}_2^r = [v_g^a, v_g^b, v_g^c]^T = v_g^{\text{abc}}$, and we have the same collection of states as the model in (9), except with the dynamics of the states being governed with $\kappa = \hat{\kappa}$. Consequently, $A^r \in \mathbb{R}^{15 \times 15}$, $B^r \in \mathbb{R}^{15 \times 2}$, $g^r : \mathbb{R}^{15} \times \mathbb{R}^3 \rightarrow \mathbb{R}^{15}$ have the same structure as (9), except with power-scaling parameter $\hat{\kappa}$.

Proposition 2. Let i_{ol}^d and i_{ol}^q denote the output current of the ℓ -th inverter. Then, the output current of the reduced-order inverter is given by

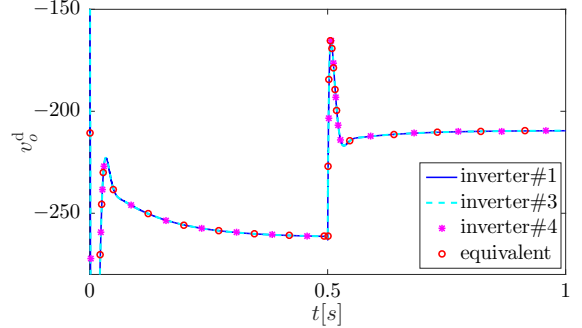
$$i_o^{\text{dr}}(t) = \sum_{\ell=1}^N i_{ol}^d, \quad i_o^{\text{qr}}(t) = \sum_{\ell=1}^N i_{ol}^q, \quad (19)$$

where i_o^{dr} and i_o^{qr} denote the output current of the reduced-order inverter.

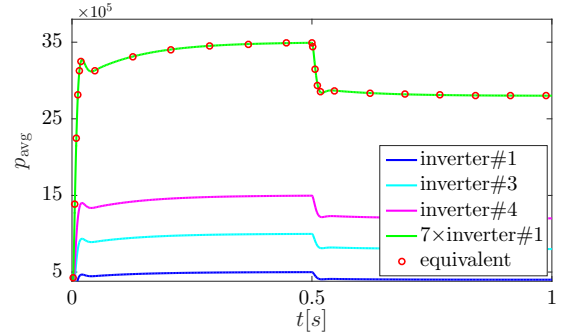
Proof. Let $i_o^{\text{d}0}$ and $i_o^{\text{q}0}$ denote the current output of inverter with nominal power rating. Since the reduced-order inverter has power-scaling factor of $\hat{\kappa}$, $i_o^{\text{dr}}(t) = \hat{\kappa} i_o^{\text{d}0}(t)$ and $i_o^{\text{qr}}(t) =$



(a) d-axis output current



(b) d-axis output voltage



(c) Output power

Fig. 2: Simulation results for three-phase inverter system demonstrating the validity of the scaling and model-reduction procedure.

$\hat{\kappa} i_o^{\text{q}0}(t)$ for $t \geq t_0$. By the definition of $\hat{\kappa}$ and the scaling of output current, the following equations hold for $t \geq t_0$:

$$i_o^{\text{dr}}(t) = \hat{\kappa} i_o^{\text{d}0}(t) = \sum_{\ell=1}^N \kappa_\ell i_{ol}^{\text{d}0}(t) = \sum_{\ell=1}^N i_{ol}^d(t),$$

and similarly for the q component of the current. \square

IV. SIMULATION RESULTS

In this section, we simulate a system of 4 parallel-connected inverters alongside the reduced-order equivalent inverter model, as illustrated in Fig. 1b. For the multi-inverter system, $N = 4$, with power-scaling parameters: $\kappa_1 = \kappa_2 = 1$, $\kappa_3 = 2$, and $\kappa_4 = 3$. The reduced-order inverter model is given initial conditions as prescribed in Proposition 2. The power and RMS voltage ratings are 500kW and 288V, respectively,

and correspond to a Siemens SINVERT PVS500 inverter [15]. During the time-domain simulation, we let $q^* = 0\text{VAR}$. The filter and controller parameters are taken from [9]. We introduce a step change in p^* from 500kW to 400kW at $t = 0.5\text{s}$. Figure 2 shows the output current, voltage, and active power dynamics of the individual and equivalent (i.e. reduced-order) inverters. Note that the plots for inverter#2 are omitted since it has the same scaling factor as inverter#1, and therefore their plots are identical. To validate the equivalent inverter and its correspondence to the individual inverters, we also plot the output current and power of the unscaled inverter (i.e. inverter#1) scaled by a factor of 7. We can see that they satisfy the following scaling properties: $i_o^{\text{dr}}(t) = 7i_{o1}^{\text{d}}(t)$, $v_o^{\text{dr}}(t) = v_{o1}^{\text{d}}(t)$, and $p_{\text{avg}}^{\text{r}}(t) = 7p_{\text{avg}1}(t)$.

V. CONCLUDING REMARKS AND DIRECTIONS FOR FUTURE WORK

In this paper, we derived a reduced-order structure-preserving model for parallel-connected grid-tied three-phase inverters. In particular, it was shown that N parallel inverters with heterogeneous power ratings can be modeled as a single inverter with an equivalent power rating equal to the sum of the individual inverter ratings. At its foundation, the proposed reduced order model is built on a set of scaling laws that prescribe how the filter and controller parameters of an individual inverter change with power rating. Ultimately, we showed that N parallel inverters that adhere to such scaling laws can be represented as one equivalent inverter whose output terminal behavior is identical to the original multi-inverter system. The next step of our study is to extend this result for the system of inverters with arbitrary electrical networks.

APPENDIX

In order to show the entries of matrices A , B , and function $g(\underline{x}, \underline{u}_2)$ compactly, let us permute the state vector as $\hat{\underline{x}} := [i_l^{\text{d}}, i_l^{\text{q}}, i_o^{\text{d}}, i_o^{\text{q}}, v_o^{\text{d}}, v_o^{\text{q}}, \gamma^{\text{d}}, \gamma^{\text{q}}, p_{\text{avg}}, q_{\text{avg}}, \phi_p, \phi_q, v_{\text{PLL}}, \phi_{\text{PLL}}, \delta]^{\text{T}}$, and the permuted dynamics are given by

$$\dot{\hat{\underline{x}}} = \hat{A}\hat{\underline{x}} + \hat{B}\underline{u}_1 + \hat{g}(\hat{\underline{x}}, \underline{u}_2), \quad (20)$$

Suppose we partition the permuted state vector as $\hat{\underline{x}} = [\underline{x}_{LCL}^{\text{T}}, \underline{x}_{CC}^{\text{T}}, \underline{x}_{PC}^{\text{T}}, \underline{x}_{PLL}^{\text{T}}]^{\text{T}}$, where $\underline{x}_{LCL} = [i_l^{\text{d}}, i_l^{\text{q}}, i_o^{\text{d}}, i_o^{\text{q}}, v_o^{\text{d}}, v_o^{\text{q}}]^{\text{T}}$, $\underline{x}_{CC} = [\gamma^{\text{d}}, \gamma^{\text{q}}]^{\text{T}}$, $\underline{x}_{PC} = [p_{\text{avg}}, q_{\text{avg}}, \phi_p, \phi_q]^{\text{T}}$, and $\underline{x}_{PLL} = [v_{\text{PLL}}, \phi_{\text{PLL}}, \delta]^{\text{T}}$. Then, we can write (20) as

$$\begin{bmatrix} \dot{\underline{x}}_{LCL} \\ \dot{\underline{x}}_{CC} \\ \dot{\underline{x}}_{PC} \\ \dot{\underline{x}}_{PLL} \end{bmatrix} = \begin{bmatrix} A_{LCL} & A_{LCL}^{\text{CC}} & A_{LCL}^{\text{PC}} & \mathbf{0}_{6 \times 3} \\ A_{CC}^{\text{LCL}} & \mathbf{0}_{2 \times 2} & A_{CC}^{\text{PC}} & \mathbf{0}_{2 \times 3} \\ \mathbf{0}_{4 \times 6} & \mathbf{0}_{4 \times 2} & A_{PC} & \mathbf{0}_{4 \times 3} \\ \mathbf{0}_{4 \times 6} & \mathbf{0}_{4 \times 2} & \mathbf{0}_{4 \times 4} & A_{PLL} \end{bmatrix} \begin{bmatrix} \underline{x}_{LCL} \\ \underline{x}_{CC} \\ \underline{x}_{PC} \\ \underline{x}_{PLL} \end{bmatrix} + \begin{bmatrix} B_{LCL} \\ B_{CC} \\ B_{PC} \\ \mathbf{0}_{3 \times 2} \end{bmatrix} \underline{u}_1 + \hat{g}(\hat{\underline{x}}, \underline{u}_2),$$

where the nonzero submatrices A_{LCL} , A_{LCL}^{CC} , A_{LCL}^{PC} , A_{CC}^{LCL} , A_{CC}^{PC} , A_{PC} , A_{PLL} , B_{LCL} , B_{CC} , and B_{PC} are given by

$$A_{LCL} = \begin{bmatrix} -\frac{k_{id}^p + r_f}{L_f} & 0 & 0 \\ 0 & -\frac{k_{iq}^p + r_f}{L_f} & 0 \\ 0 & 0 & -\frac{r_c}{L_c} \\ 0 & 0 & -2\pi \times 60 \\ -R_d \frac{k_{id}^p + r_f}{L_f} + \frac{1}{C_f} & -(2\pi \times 60)R_d & R_d \frac{r_c}{L_c} - \frac{1}{C_f} \\ (2\pi \times 60)R_d & -R_d \frac{k_{iq}^p + r_f}{L_f} + \frac{1}{C_f} & 0 \\ 0 & -\frac{1}{L_f} & 0 \\ 0 & 0 & -\frac{1}{L_f} \\ 2\pi \times 60 & \frac{1}{L_c} & 0 \\ -\frac{r_c}{L_c} & 0 & \frac{1}{L_c} \\ 0 & -R_d(\frac{1}{L_c} + \frac{1}{L_f}) & 2\pi \times 60 \\ R_d \frac{r_c}{L_c} - \frac{1}{C_f} & -2\pi \times 60 & -R_d(\frac{1}{L_c} + \frac{1}{L_f}) \end{bmatrix},$$

$$A_{LCL}^{\text{CC}} = \begin{bmatrix} \frac{k_{id}^i}{L_f} & 0 \\ 0 & \frac{k_{iq}^i}{L_f} \end{bmatrix},$$

$$A_{LCL}^{\text{PC}} = \begin{bmatrix} 0 & -\frac{k_{id}^p}{L_f} k_Q^p & 0 & \frac{k_{id}^p}{L_f} k_Q^i \\ -\frac{k_{iq}^p}{L_f} k_P^p & 0 & \frac{k_{iq}^p}{L_f} k_P^i & 0 \end{bmatrix},$$

$$A_{CC}^{\text{LCL}} = \begin{bmatrix} -1 & 0 & 0 & 0 & 0 & 0 \\ 0 & -1 & 0 & 0 & 0 & 0 \end{bmatrix},$$

$$A_{CC}^{\text{PC}} = \begin{bmatrix} 0 & -k_Q^p & 0 & k_Q^i \\ -k_P^p & 0 & k_P^i & 0 \end{bmatrix},$$

$$A_{PC} = \begin{bmatrix} -\omega_c & 0 & 0 & 0 \\ 0 & -\omega_c & 0 & 0 \\ -1 & 0 & 0 & 0 \\ 0 & -1 & 0 & 0 \end{bmatrix}, A_{PLL} = \begin{bmatrix} -\omega_c, \text{PLL} & 0 & 0 \\ -1 & 0 & 0 \\ -k_{\text{PLL}}^p & k_{\text{PLL}}^i & 0 \end{bmatrix},$$

$$B_{LCL} = \begin{bmatrix} 0 & \frac{k_{id}^p}{L_f} k_Q^p \\ \frac{k_{iq}^p}{L_f} k_P^p & 0 \\ 0 & 0 \\ 0 & 0 \\ 0 & \frac{k_{id}^p}{L_f} k_Q^p R_d \\ \frac{k_{iq}^p}{L_f} k_P^p R_d & 0 \end{bmatrix}, B_{CC} = \begin{bmatrix} 0 & k_Q^p \\ k_P^p & 0 \end{bmatrix},$$

$$B_{PC} = \begin{bmatrix} 0 & 0 \\ 0 & 0 \\ 1 & 0 \\ 0 & 1 \end{bmatrix}.$$

The nonzero entries of $\hat{g}(\hat{\underline{x}}, \underline{u}_2)$ are given by

$$\begin{aligned} \hat{g}_3(\hat{\underline{x}}, \underline{u}_2) &= (-k_{\text{PLL}}^p v_{\text{PLL}} + k_{\text{PLL}}^i \phi_{\text{PLL}}) i_o^{\text{q}} - \frac{\kappa}{L_c} v_g^{\text{d}}(\hat{\underline{x}}, \underline{u}_2), \\ \hat{g}_4(\hat{\underline{x}}, \underline{u}_2) &= (k_{\text{PLL}}^p v_{\text{PLL}} - k_{\text{PLL}}^i \phi_{\text{PLL}}) i_o^{\text{d}} - \frac{\kappa}{L_c} v_g^{\text{q}}(\hat{\underline{x}}, \underline{u}_2), \\ \hat{g}_5(\hat{\underline{x}}, \underline{u}_2) &= (-k_{\text{PLL}}^p v_{\text{PLL}} + k_{\text{PLL}}^i \phi_{\text{PLL}}) (-R_d i_l^{\text{q}} + v_o^{\text{q}}) \\ &\quad + \frac{R_d}{L_c} v_g^{\text{d}}(\hat{\underline{x}}, \underline{u}_2), \\ \hat{g}_6(\hat{\underline{x}}, \underline{u}_2) &= (k_{\text{PLL}}^p v_{\text{PLL}} - k_{\text{PLL}}^i \phi_{\text{PLL}}) (-R_d i_l^{\text{d}} + v_o^{\text{d}}) \end{aligned}$$

$$\begin{aligned}
& + \frac{R_d}{L_c} v_g^q(\hat{\underline{x}}, \underline{u}_2), \\
\hat{g}_9(\hat{\underline{x}}, \underline{u}_2) &= \frac{3}{2} \omega_c (v_g^d(\hat{\underline{x}}, \underline{u}_2) i_o^d + v_g^q(\hat{\underline{x}}, \underline{u}_2) i_o^q), \\
\hat{g}_{10}(\hat{\underline{x}}, \underline{u}_2) &= \frac{3}{2} \omega_c (v_g^q(\hat{\underline{x}}, \underline{u}_2) i_o^d - v_g^d(\hat{\underline{x}}, \underline{u}_2) i_o^q), \\
\hat{g}_{13}(\hat{\underline{x}}, \underline{u}_2) &= \omega_{c, PLL} v_g^d(\hat{\underline{x}}, \underline{u}_2), \\
\hat{g}_{15}(\hat{\underline{x}}, \underline{u}_2) &= 2\pi \times 60,
\end{aligned}$$

where $v_g^d(\hat{\underline{x}}, \underline{u}_2)$ and $v_g^q(\hat{\underline{x}}, \underline{u}_2)$ are given by

$$\begin{aligned}
v_g^d(\hat{\underline{x}}, \underline{u}_2) &= \frac{2}{3} \left(\cos(\delta) v_g^a + \cos\left(\delta - \frac{2\pi}{3}\right) v_g^b \right. \\
& \quad \left. + \cos\left(\delta + \frac{2\pi}{3}\right) v_g^c \right), \\
v_g^q(\hat{\underline{x}}, \underline{u}_2) &= \frac{2}{3} \left(\sin(\delta) v_g^a + \sin\left(\delta - \frac{2\pi}{3}\right) v_g^b \right. \\
& \quad \left. + \sin\left(\delta + \frac{2\pi}{3}\right) v_g^c \right).
\end{aligned}$$

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