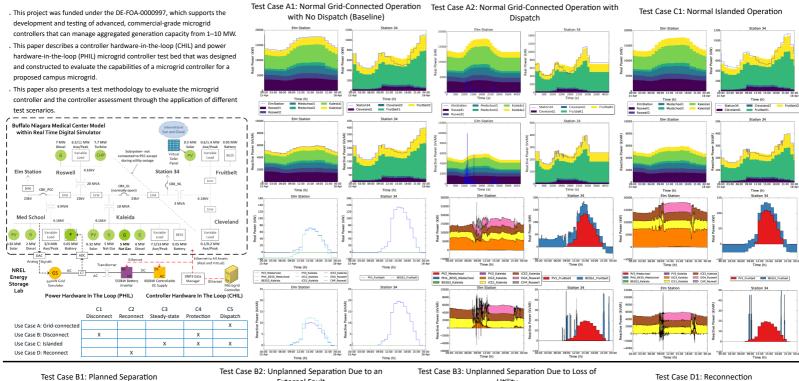
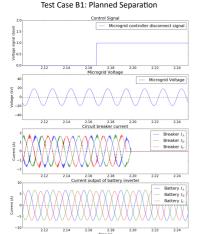
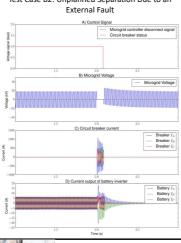


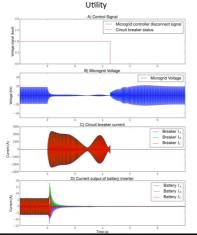
Hardware-in-the-Loop Test Bed and Test Methodology for Microgrid Controller Evaluation

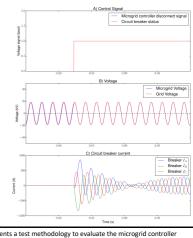
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bilities of a microgrid controller.

2. The test bed incorporates both CHIL and PHIL elements in the experimental setup. A campus microgrid was modeled in a digital real-time simulator, and communications were enabled among the microgrid controller hardware and the simulated assets, circuit breakers, and loads as well as with the battery inverter hardware via a PHIL interface

3. This paper also presents a test methodology to evaluate the microgrid controller 1. This paper presented the development of a test bed for evaluating the functional capa- through the application of different test scenarios. Hardware-in-the-loop simulation results provide insight into the test bed's capabilities and the types of evaluations that could be performed by using it. Such a test bed is valuable to help microgrid controller providers, microgrid developers, and owners evaluate performance prior to field deployment.