

# Development of Next-Generation High-Efficiency Silicon Solar Cells

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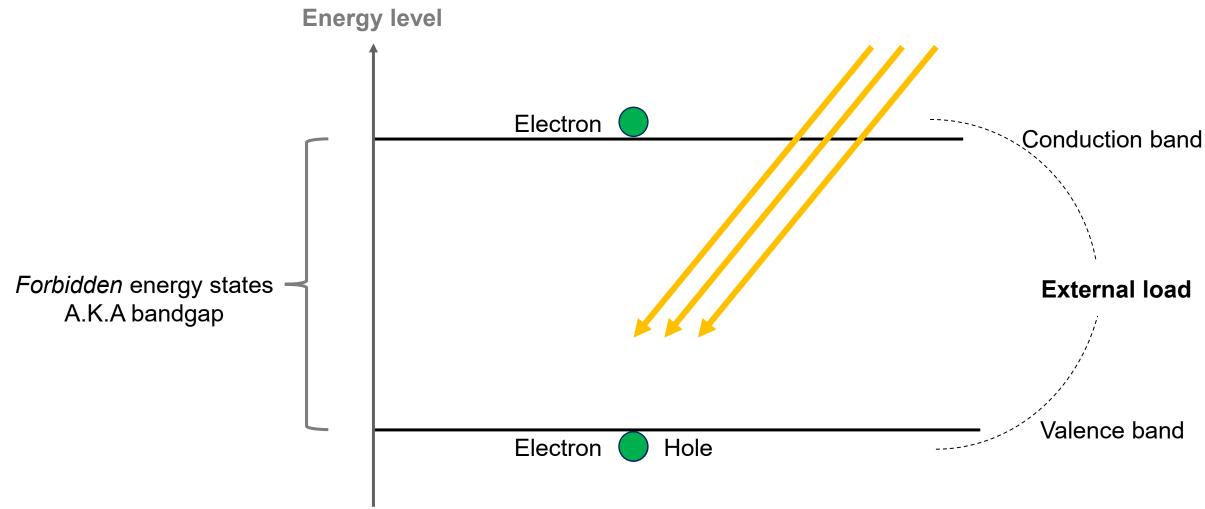
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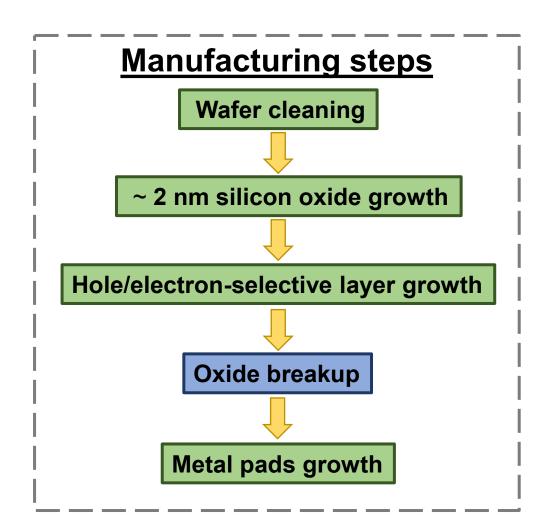
#### **Solar Cells 101**

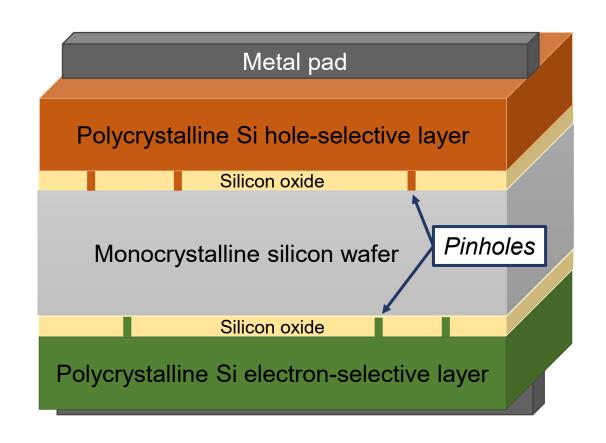






## Next-generation High-Efficiency Si Solar Cell









## **Chemical Etching to Expose Nano-pinholes**

Nano-pinholes are not observable even with high-resolution electron microscopy.

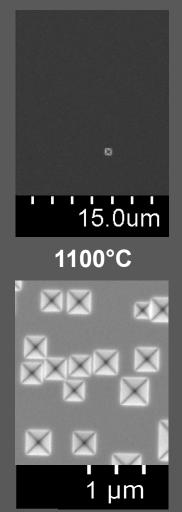
"Magnified" pinhole Polycrystalline silicon Silicon oxide Monocrystalline silicon wafer Silicon oxide Polycrystalline layer



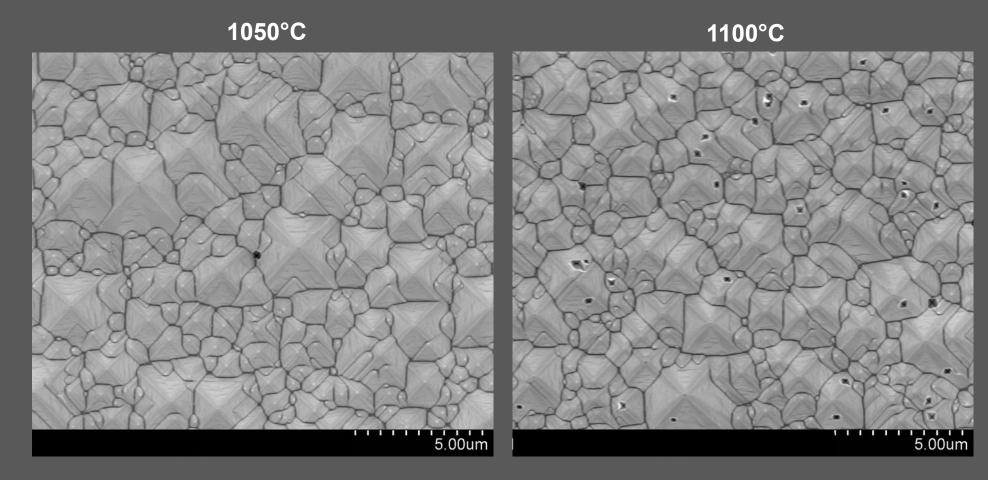
**Reference**: Tetzlaff D. et al. "A simple method for pinhole detection in carrier selective POLO-junctions for high efficiency silicon solar cells," *Solar Energy Materials and Solar Cells*, 2017.

1050°C

### "Magnified" pinholes



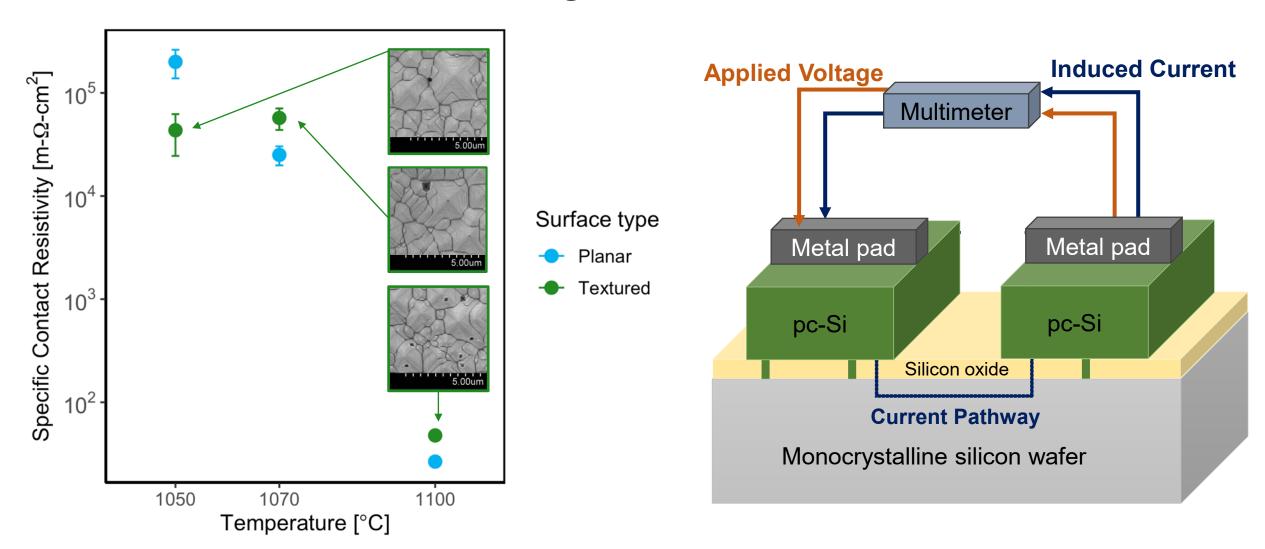
"Magnified" pinholes on planar samples



"Magnified" pinholes on textured samples processed at different temperatures



#### **Conductivity measurements**





#### Summary

- 1. Nano-pinholes enable electrical conduction across the insulating silicon oxide layer.
- 2. Chemical etching is utilized to visualize nano-pinhole formation in planar and textured samples.
- 3. We developed a fabrication process for high-efficiency silicon solar cells, providing a platform to develop controlled pinhole formation methods.



#### Acknowledgements

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