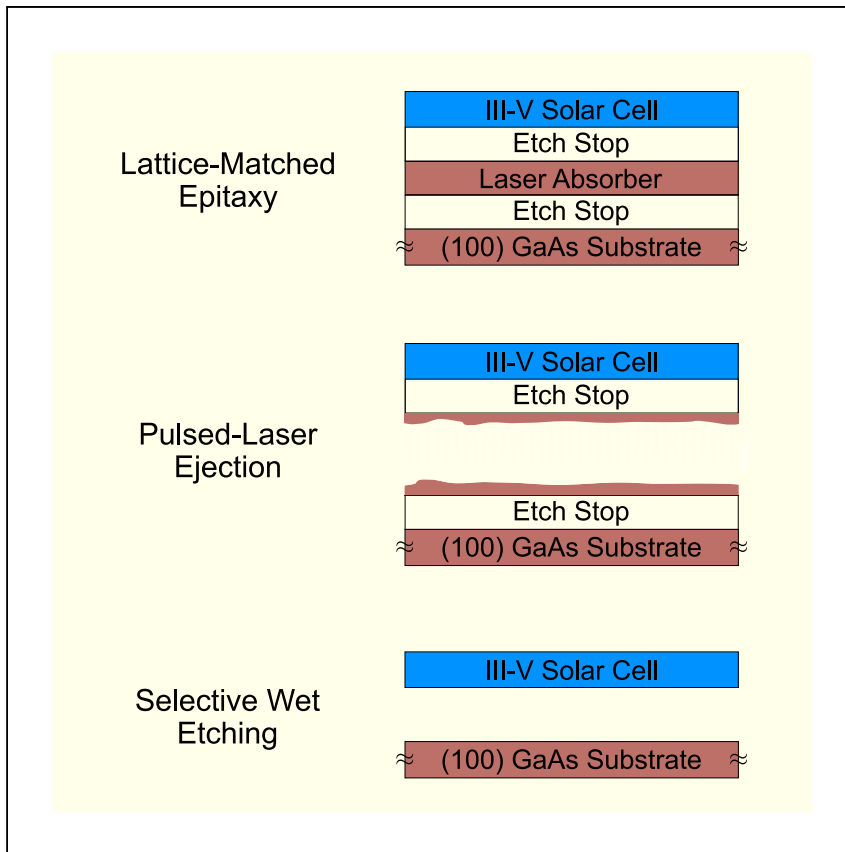


Report

Pulsed laser ejection of single-crystalline III-V solar cells from GaAs substrates



The best solar cells use single crystal, III-V active layers that are grown on GaAs wafers. Reeves et al. pop off a μm -thin, III-V multilayer from a GaAs wafer with a laser pulse, then use fast surface-processing operations to turn the crystalline thin film into a high-performing photovoltaic device.

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Highlights

Thin, single-crystalline III-V solar cells are removed from GaAs substrates

A laser pulse ejects a lattice-matched III-V multilayer from the GaAs wafer

High-performing solar cells are ejected and finished in minutes

Lattice-matched optical selectivity provides diverse, scalable processing paths

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Report

Pulsed laser ejection of single-crystalline III-V solar cells from GaAs substrates

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SUMMARY

The best III-V solar cells start out as single-crystalline multilayers on GaAs substrates. Separating these multilayers from their growth substrate enables higher performance and wafer reuse, which are both critical for terrestrial III-V solar cell viability. Here, we remove rigidly bonded, $16 \text{ mm}^2 \times 3.5 \text{ }\mu\text{m}$ thick devices from a GaAs substrate using an unfocused Nd:YAG laser pulse. The pulse is absorbed by a low-band-gap, lattice-matched layer below the device, driving an ablation event that ejected the crystalline multilayer from the substrate. Minutes of selective wet-chemical etching and device finishing yield a 0.1 cm^2 device with a 17.4% power conversion efficiency and open-circuit voltage of 1.07 V, using AM1.5 direct with no anti-reflection coating. We show that the performance is comparable to similar cells produced via conventional processes. We discuss unique process characteristics, such as the potential to separate wafer-sized solar cells per laser pulse.

INTRODUCTION

Thin film transfer and wafer recovery processes are essential for manufacturing single-crystal III-V solar cells. III-V substrates are typically two to three orders of magnitude thicker than the active photovoltaic layers,¹ and III-V wafer costs are high because, for example, III-V elements and compounds are not abundant.² They are also toxic, carcinogenic,³ and fragile,⁴ and III-V wafer manufacturing utilizes specialized, encapsulated crystal pulling techniques for relatively small wafer diameters and with high material losses.^{4,5} Yet, all record-holding solar cells are made from single-crystal III-V thin films that were grown on III-V substrates.^{6–9} The proliferation of the highest-quality solar cells therefore depends, in part, on rapid and inexpensive processes that separate single-crystal thin films from III-V substrates while preserving wafer surfaces for regrowth.^{10,11}

Driven in part by demand for lower-cost III-V photovoltaics, various III-V single-crystalline thin film transfer solutions have been developed, all of which utilize some combination of epitaxy, mechanical peeling, and selective chemical etching. Wet-chemical etch-rate ratios between lattice-matched alloys can exceed 10^6 .^{12,13} This etch selectivity is utilized to separate thin films from growth substrates, for example by laterally etching a 10–100 nm thick AlAs^{14,15} or an AlInP¹⁶ layer grown between a GaAs substrate and the device layers. The lateral etches become diffusion limited and stall unless the film is peeled away during etching and then proceed only at order $1\text{--}10 \text{ mm h}^{-1}$.¹⁷ With spalling, a mechanical separation process, a tensile stressor layer on the thin film surface pulls open a lateral crack beneath the device layers.¹⁸ Unfortunately, for (100) III-V substrates, cracks opening along a {100} plane

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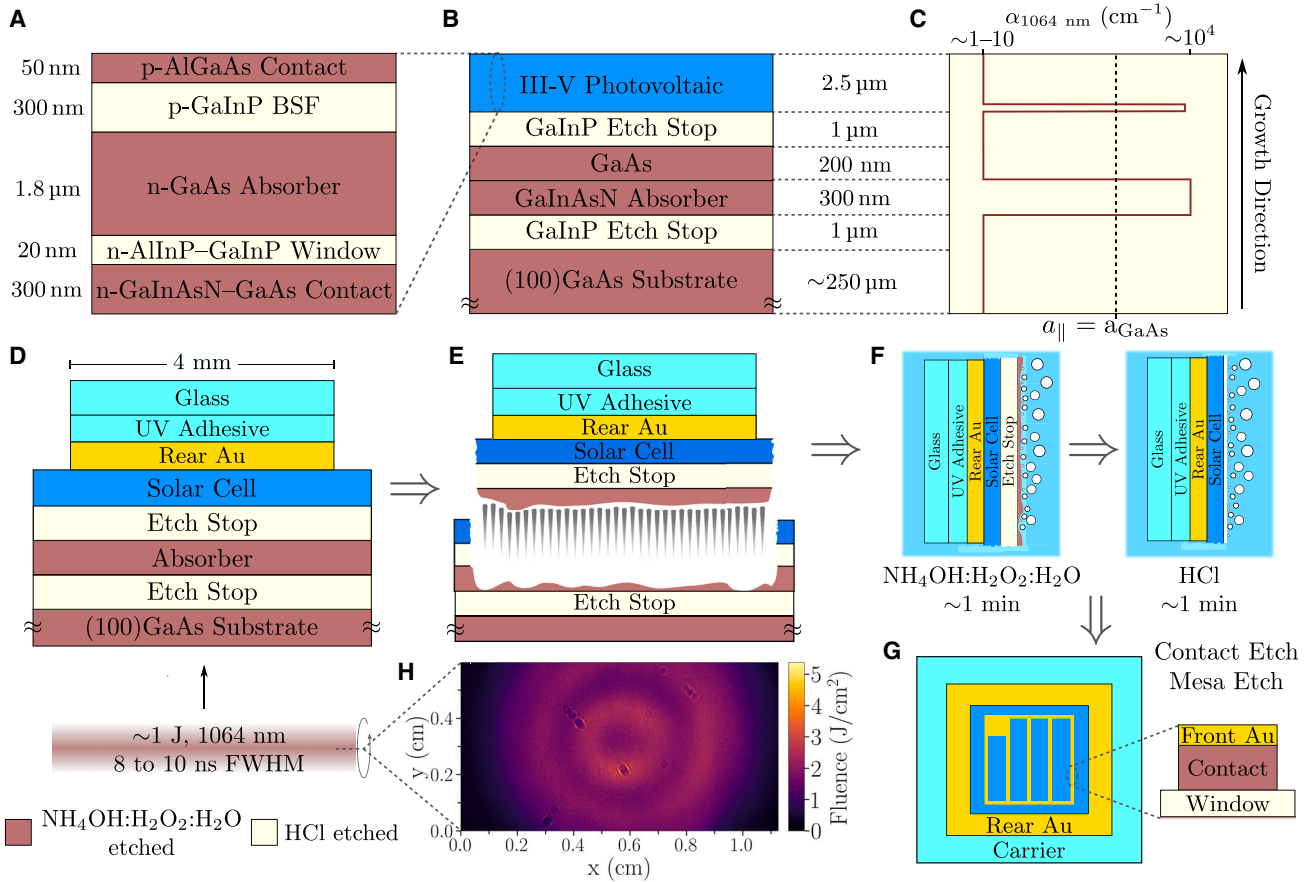


Figure 1. Overview of device fabrication with pulsed-laser crystal ejection

(A) This work used GaAs-based, single-heterojunction, inverted, lattice-matched, III-V photovoltaics.

(B) The photovoltaic was synthesized in line with the layers used for device ejection and finishing (a lattice-matched, GaInP-GaInAsN-GaAs-GaInP multilayer).

(C) The relatively narrow, direct electronic band gap of the GaInAsN absorber created a sharp absorption gradient for a Nd:YAG laser pulse inside of the crystal.

(D) After depositing an Au|Ti contact layer onto the p-AlGaAs, the Ti (not shown) was bonded to glass, and the surrounding metal etched away. The specimen was then positioned into the path of a normally incident Nd:YAG laser pulse.

(E) The laser pulse drove an ablation event inside of the crystal, which caused the photovoltaic device structure to eject from the substrate.

(F) After ejection, the melt debris and etch stop were etched away in minutes with room temperature, chemically selective etchants. Prior to etching, the glass was bonded to a larger glass carrier to improve device handling (not shown for simplicity).

(G) After electroplating the Au grid, the exposed front GaInAsN contact layer was etched down to the AlInP window. The mesa was defined with photolithography and etched, similar to (F).

(H) Our laser spatial filter could produce single- or multimode laser pulse spatial profiles. Here, we used multimode pulses to maximize transmitted energy. The small Fresnel diffraction spots rotated around the profile when rotating imaging optics and are therefore not intrinsic to the laser spatial profile.

will redirect onto the lower-energy {110} planes and potentially into device layers and the substrate.¹⁹ Variations on spalling and chemical liftoff processes seek to address crack-confinement,^{20,21} stress management,²² lateral etching rates,¹⁷ and surface effects.¹⁶

Here, we eject III-V photovoltaic devices from a GaAs wafer using a laser pulse (Figure 1) by exploiting wavelength-dependent optical absorption $\alpha(\lambda)$ differences between conventional, lattice-matched III-V layers.^{23–26} Unlike previous work, we demonstrate the nearly instantaneous separation of rigidly bonded, compositionally and electronically complex thin films, as well as characteristic, rapid finishing

operations to produce high-performing devices. The device structure is a multilayer of III-V alloys, with sharp, optoelectronic property gradients, and active layers are single crystals of order 1 nm to 1 μm thick. Individual layers provide critical functionalities,^{27–29} such as surface passivation with indirect, wide-band-gap III-V windows, carrier-selective transport, Ohmic contacting, and, now, additional degrees of freedom to optically split the active layers from the GaAs substrate and finish surfaces with minutes of wet etching. We compare our prototype, laser-ejected solar cells to specimens produced at the National Renewable Energy Laboratory (NREL) from mature fabrication processes and comparable metalorganic vapor phase epitaxy (MOVPE) growth parameters.

RESULTS AND DISCUSSION

Solar cell fabrication and ejection

We synthesized standard III-V thin films via MOVPE at NREL (Figures 1A, 1B, and S1; supplemental experimental procedures; Note S1). Beginning with a single-side polished (100) GaAs substrate, we grew a 1 μm thick $\text{Ga}_{0.49}\text{In}_{0.51}\text{P}$ etch stop, a 300 nm $\text{Ga}_{0.93}\text{In}_{0.07}\text{As}_{0.98}\text{N}_{0.02}$ absorber layer, a 200 nm GaAs spacing layer, and another 1 μm thick $\text{Ga}_{0.49}\text{In}_{0.51}\text{P}$ etch stop (all thicknesses and compositions are nominal). Growth continued with an inverted rear-heterojunction III-V photovoltaic cell³⁰ consisting of an n- $\text{Ga}_{0.97}\text{In}_{0.03}\text{As}_{0.99}\text{N}_{0.01}$ front contact layer, n- $\text{Al}_{0.52}\text{In}_{0.48}\text{P}$ | $\text{Ga}_{0.49}\text{In}_{0.51}\text{P}$ window layers, an n-GaAs absorber, a p- $\text{Ga}_{0.49}\text{In}_{0.51}\text{P}$ heterojunction base, and a p- $\text{Al}_{0.3}\text{Ga}_{0.7}\text{As}$ rear contact layer. Relative to mature baseline devices, the GaInP-GaInAsN-GaAs-GaInP layer set was the only growth modification necessary for laser ejection. The two GaInAsN layers were slightly different: the laser absorber layer had $\sim 2\%$ N on the group V sub-lattice and $\sim 7\%$ In on the group III sub-lattice to lattice match the alloy, while the front contact layer had $\sim 1\%$ N and 3% In to lattice match. Photoluminescence and X-ray diffraction results from the $\text{Ga}_{0.93}\text{In}_{0.07}\text{As}_{0.98}\text{N}_{0.02}$ absorber layer were consistent with a pseudomorphic alloy, with a band gap of approximately 1.07 eV (Note S2; Figure S2), which is 0.1 eV less than the laser's 1.17 eV photon energy. As shown in Figure 1C, based on typical behaviors of direct-band-gap III-V alloys,³¹ the GaInAsN absorber (and GaInAsN front contact) likely had absorption coefficients $\alpha(1,064 \text{ nm})$ of about 10^4 cm^{-1} . However, $\alpha(1,064 \text{ nm})$ for the other compounds were orders of magnitude lower because their electronic band gaps were greater than the photon energy, although their absorption was not zero due to the non-linear processes that occur at our order 100 MW cm^{-2} intensities.^{32,33} For all layers, the lattice matching preserved the in-plane lattice parameter $a_{\parallel} = a_{\text{GaAs}}$, preventing strain-induced defects such as misfit dislocations.³⁴

After MOVPE, we prepared the specimen for crystal ejection. The substrate was single-side polished for better *in situ* monitoring of the curvature and stress during MOVPE, but because the laser pulse would travel through the substrate, the substrate was polished after epitaxy to produce a specular surface and an approximate thickness of 250 μm . The specular surface was protected with photoresist until laser processing. The AlGaAs was stripped of surface oxides via wet etching, followed by e-beam evaporation of a 500 nm rear Au reflective contact, then a 40 nm Ti adhesion layer. 4 \times 4 mm glass coverslips were bonded to the Ti with UV-curing adhesive and direct-write UV photolithography.

After removing the surrounding Ti and Au with wet etching, the contacted and mounted device layers were inserted normal to the optical path of a commercial, Q-switched, Nd:YAG laser, with the polished wafer surface facing the laser. The laser

produced nominally 1,064 nm wavelength, 8–10 ns full width at half maximum (FWHM) pulses (Figures 1D, 1H, and S3; Note S3). Sharp variations in the laser pulse spatial intensity profile were minimized with a vacuum-pinhole spatial filter, yielding pulses with the characteristic spatial fluence profile shown in Figure 1H, and pulse energies of about 1.1 ± 0.1 J (the small diffraction spots in Figure 1F rotated with imaging optics and are therefore imaging artifacts). After coarse alignment by transmission imaging of highly attenuated pulses, we removed attenuation filters and ejected the specimen (Note S3; Figure S6). We observed single-shot specimen ejection (Figure S4), but if a single pulse did not eject a specimen—due to misalignment or pulse energy fluctuation, for example—the specimen was translated by order 100 μm before firing another pulse.

The ejected specimen's glass was adhered to a glass carrier for selective wet etching and device finishing (Figures 1F and 1G). The melt debris and GalnP etch stop were removed with their respective selective etchant (the substrate could be treated analogously). We then used direct-write photolithography to develop the front contact pattern into photoresist and selectively electroplated a thin Ni adhesion layer and nominally 2 μm Au into the pattern to form the front metal grid (Figure S7). The exposed GalnAsN contact layer was removed via wet etching, and finally, a mesa was defined via photolithography and additional selective etching.

Device characterization

The device's photovoltaic performance was measured at NREL. Figure 2 shows the testing configuration, current density vs. voltage (J-V) curves under the AM1.5 direct spectrum at $1,000 \text{ W/m}^2$, dark J-V, and external quantum efficiency data for our best cells. We compare our laser-ejected devices with similar baseline cells grown in the same MOVPE reactor and fabricated via NREL's mature GaAs processes, where the substrate was chemically etched away.^{35,36} As summarized in Table 1, our prototype-ejected devices demonstrated excellent performance metrics, near parity with the baseline. With no anti-reflection coatings, our highest performing cell showed a conversion efficiency $\eta = (17.4 \pm 0.5)\%$, a short circuit current density $J_{sc} = (19.8 \pm 0.6) \text{ mA/cm}^2$, an open circuit voltage $V_{oc} = 1.07 \text{ V}$, a fill factor $FF = 0.82$, and a device area $A = 0.105 \text{ cm}^2$. The external quantum efficiency data also show comparable performance, with characteristic, sharp electronic band edges, and Fabry-Perot interference fringes from the single-crystalline uniform multilayers and highly reflective back mirror. The quantum efficiency differences between the etched and ejected samples are on the order of a few percentage points at all wavelengths. The dark J-V shows a difference between the etched and ejected samples. Both sets of samples exhibit normal $n = 1$ diode behavior at high voltage and $n = 2$ behavior at low voltage, but the ejected samples show shunt-like behavior at intermediate voltages, which likely explains the reduced fill factors. The shunt-like behavior is emphasized by the log-normal plot. It would appear to be a non-linear shunt that does not affect the slope of the J-V curve (Figure 2B) near 0 V, as in a more typical shunt. Understanding and potentially mitigating this behavior was beyond the scope of this study. As discussed later, this would require lowering the total experimental uncertainty, producing larger sample sizes, and characterizing with additional techniques such as scanning electron and atomic force microscopies. Were this behavior unavoidable, it would be particularly deleterious for low-intensity applications.

We observed finished, ejected devices with bright-field, Nomarski differential interference contrast, and dark-field microscopies. Despite our inhomogeneous, multimode laser pulses (Figure 1H), we did not observe any thin film cracking or

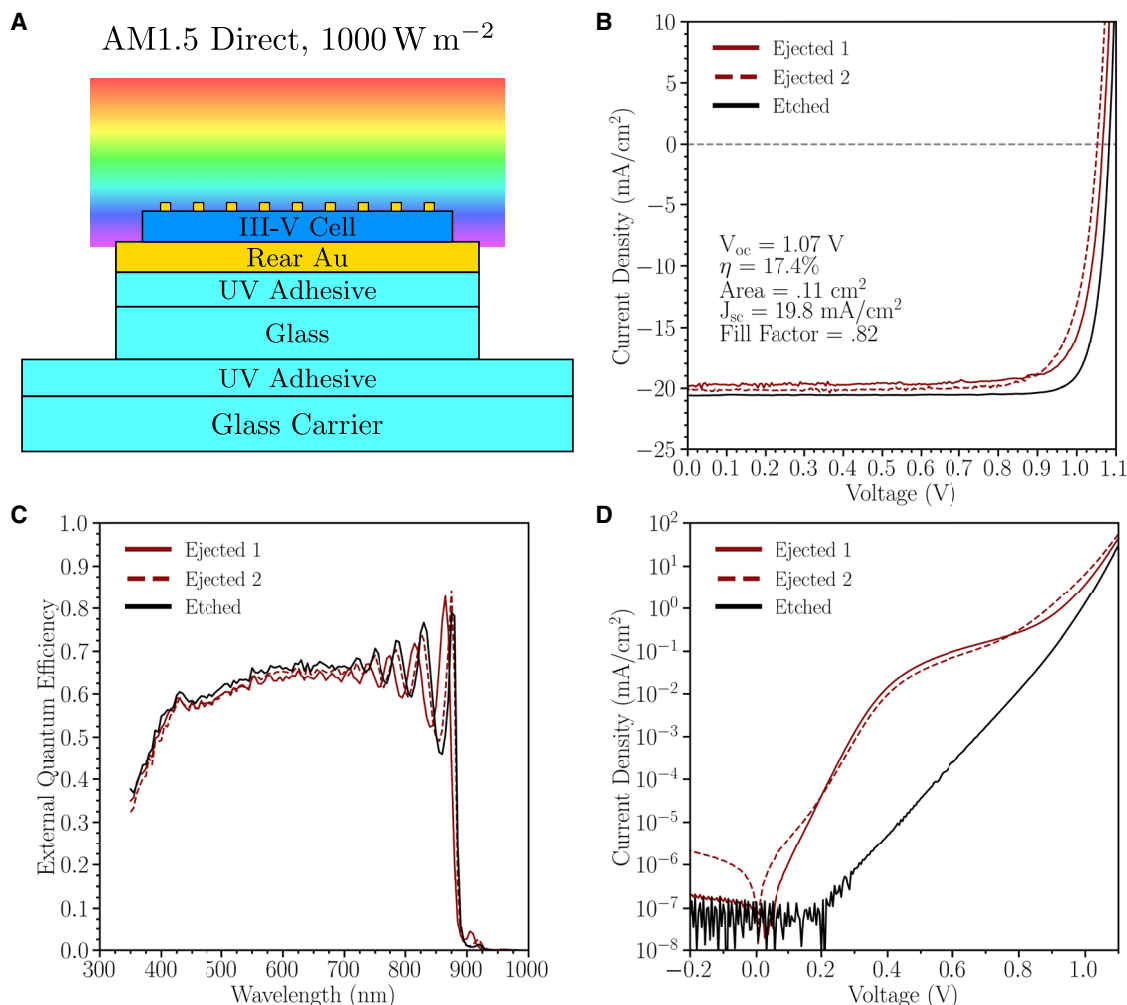


Figure 2. Results from optoelectronic device characterization

(A) J-V tests on the final ejected device structures were measured under AM1.5 direct, 1,000 W/m² conditions at 25°C.

(B) The ejected and etched cells showed excellent, comparable performance. The performance metrics for our best-performing ejected cell are noted in the figure and in Table 1.

(C) External quantum efficiency was comparable between etched and ejected cells.

(D) Dark curves for the ejected cells showed a non-linear shunt for the ejected devices.

other bulk damage for the device dimensions that were readily ejectable with our laser system (Figures 3A–3C and S5; Note S5). We did observe thin film damage near the ejected film edges and corners, especially after wet etching. During laser ejection, device corners were located within a few hundred microns of the sub-critical fluences near our pulse edges and could mechanically tear during ejection. Furthermore, unlike the baseline cells, our ejected devices were elevated from the

Table 1. A summary of etched and ejected device performance metrics

Device	η (%)	J_{sc} (mA/cm ²)	V_{oc} (V)	FF	Area (cm ²)
Ejected 1	17.4	19.8	1.07	0.821	0.105 (measured)
Ejected 2	17.0	19.9	1.05	0.810	0.101 (measured)
Etched	19.2	20.6	1.08	0.863	0.1005 (nominal)

Photovoltaic performance metrics for the two laser-ejected cells, and the baseline cell whose substrate was etched away with NH₄OH:H₂O₂.

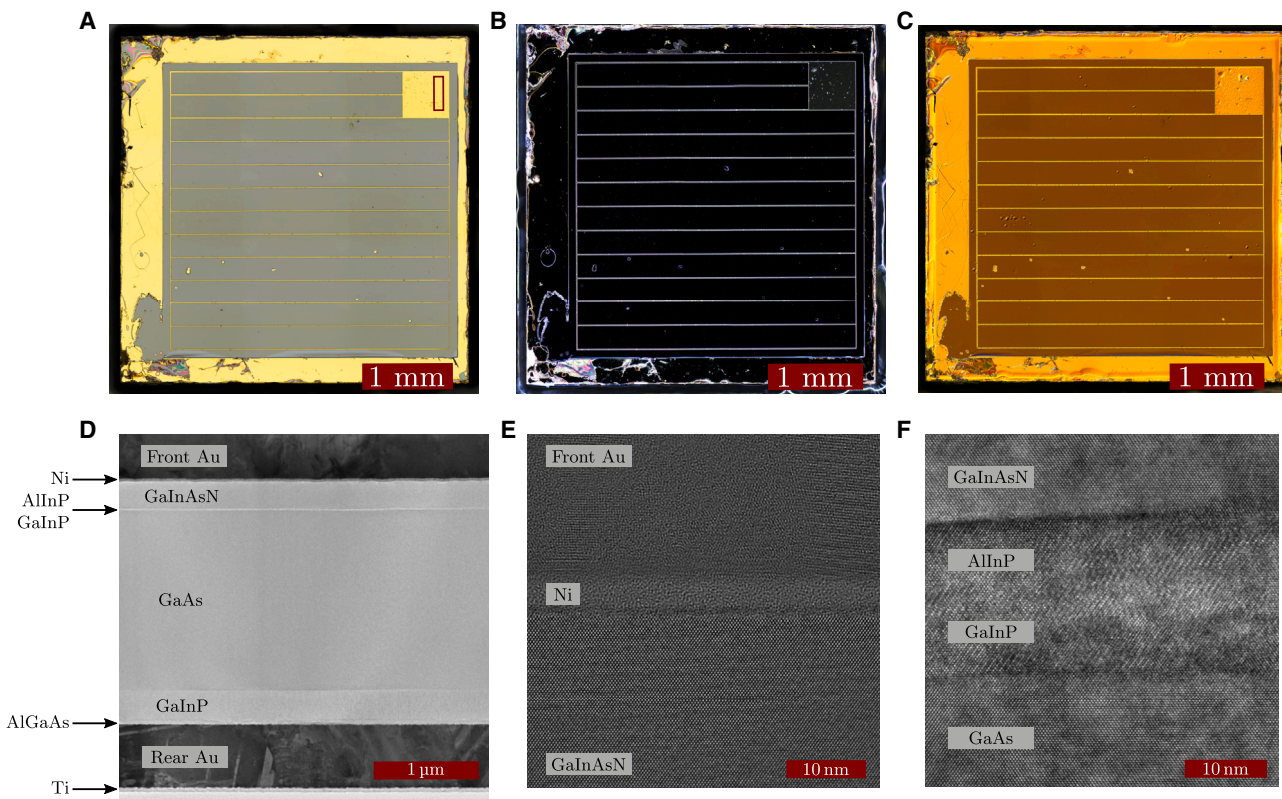


Figure 3. Optical and TEM device characterization

(A) A bright-field optical micrograph of the finished cell. The rectangle over the front contact pad shows the characteristic ion-milling location for TEM specimens. Image stitching artifacts are seen in (A)–(C).
 (B) Dark-field optical microscopy reveals a crack-free surface, as well as debris on the AllnP window.
 (C) Differential interference contrast microscopy also shows a crack-free surface, as well as electroplating roughness and rounded subsurface features not seen in (A) or (B).
 (D) A TEM cross-section of an ejected cell, showing layer thicknesses and excellent material quality. The $\langle 100 \rangle$ direction is vertical, and $\langle 110 \rangle$ is normal to the page.
 (E and F) We found no evidence of misfit dislocations or melted interfaces while searching the lamella shown in (D). The scale bars in (E) and (F) were calibrated by assuming GaAs atomic spacing in the micrographs.

surface of the glass carrier (by the Ti-bonded glass). Hence, effects such as photore-sist beading and adhesive swelling along the elevated edges complicated wet etching, photolithography, and electroplating. Larger sample sizes and statistical analyses were beyond the scope of the present study. Both will be required to optimize device performance, as well as to understand any damage mechanisms, their effects, and appropriate mitigation strategies from within the vast process parameter space. Our ~ 1 J pulse contained enough energy to eject ~ 1 cm². However, as shown in Figure 1H (and discussed in Note S3), our fluence reached about 3–4 times higher than ~ 1 J/cm² in many regions of the pulse. This variation reduced the maximum ejectable area for the system used here and subjected much of the structure to unnecessarily high fluences. We do not know the upper area for the system as described. After a single 5×5 mm specimen did not eject, we finished process development and solar cell prototyping using 4×4 mm, to conserve the precious epitaxial solar cell material. At a minimum, future experiments should explore flat laser spatial profiles—working near the critical ejection fluence—with sufficient energy and diameter to extend beyond the edges of order 1–100 cm² ejection specimens. Such large, flat, and oversized pulses would eliminate many sources

of experimental uncertainty (such as edge effects during processing and multishot ejection) and demonstrate the reliability of splitting crystals by wavelength- and epitaxially selective absorption.

Prior transmission electron microscopy (TEM) and X-ray diffraction work showed that GaAs thin films ejected from a GaAs substrate were free from misfit dislocations and had the same structural quality.²³ We used TEM to search for dislocations and disordering in a laser-ejected, finished GaAs cell (Figures 3D–3F). Using an ejected solar cell from this batch that failed during photovoltaic testing, we removed a cross-sectional lamella from between the front Au contact pad and the rear Au contact using a focused Ga ion beam and micromanipulators (Figure 3A). The transmission electron micrographs revealed the single-crystalline multilayers characteristic of high-performing III-V photovoltaic cells. We found atomically coherent interfaces and no misfit dislocations, a consequence of targeting III-V compositions that are lattice matched to GaAs (and whose composition deviations were not sufficient to exceed critical stresses during growth). These data also show that the crystalline thin films and precise interfaces persisted even though the layers were within about 1 μm of a fast, intracrystalline ablation transient. It is especially notable that both the rear Au contact and the front GaInAsN contact layer were intact after ejection, as these layers would absorb and reflect any energy transmitted during the first few ns of the laser pulse, before the absorber layer became opaque. These data support the conclusion that, for the bonding conditions, specimen design, laser system, and ejection scheme used in this work, the amplitude and duration of the thermomechanical transients during and after ejection do not necessarily damage the finished crystals.

On the substrate side, we have not demonstrated regrowth, even though substrate recovery and regrowth is one key motivation to separate device layers. Dark-field images of the post-ejection substrate clearly show that it is possible to damage the substrate through the 1 μm GaInP etch stop (Note S4; Figure S4C, left). Even with our highly inhomogeneous laser cross-section, single-shot ejection mitigates this damage considerably (Note S4; Figure S4C, right). Independent of improvements to, for example, laser pulse spatial homogeneity, laser pulse duration, and the choice of etch stop alloys, the lower etch stop layer could simply be grown thicker. The process is also particularly amenable to using multiple post-ejection, orthogonal wet etches to restore growth surfaces.³⁷ We therefore hypothesize that relatively straightforward characterization and process iteration will yield high-quality regrowth without resorting to chemo-mechanical polishing, and that total material loss per ejected area will be of the same order as the thickness melted around the absorber.

Notable technique properties

Our solar cells were removed from their growth substrate with a ~ 10 ns laser pulse and are the largest single-crystalline devices that we have ejected via pulsed laser energy. Damage was restricted to within approximately 1 μm of a pre-determined epitaxial interface, and the ejected film was only 3.5 μm thick; both of these dimensions are smaller than those we found reported for spalling.^{20,21} Crystal ejection is a special case of laser liftoff, but it differs in many ways from, for example, the laser liftoff of GaN from sapphire.³⁸ Arguably, the most striking differences are that crystal ejection divides a single crystal lattice, that this is possible by virtue of an alloy that is lattice matched to GaAs but has a relatively small band gap, and that other lattice-matched compounds can be grown around the absorber that have large wet-etch selectivities and negligible laser absorption. As we will discuss, the differences

lead to powerful scaling hypotheses for thin film crystal making. We postulate that crystal ejection's processing space has at least seven important dimensions: (1) laser wavelength, (2) pulse duration, (3) pulse energy, (4) laser spatial profile, (5) multilayer architecture (including thicknesses, compositions, and metal reflectors), (6) initial strain (intrinsic, as well as bonding-induced), and (7) surface treatments. Beyond successful ejection of these 4 × 4 mm glass|Ti|Au|III-V structures, we do not know the ultimate ejected-area limit at any point in the processing space, nor the minimum amount of material that must be sacrificed for ejection and device finishing. However, at least for 1–10 ns, 1064 nm, Nd:YAG lasers, wafer-scale pulses can be created at tens of Hz with commercial laser systems (see, for example, <https://ekspla.com/products/high-intensity-laser-systems/>). Coupled with the present results, and with the accuracy of MOVPE, this suggests that it is already possible to eject lithographically patterned arrays of mm-scale III-V devices, even if thermomechanical cracks or other issues emerge with larger, continuous thin films.

Various facets of this process demonstration motivate discussions about scalability and fundamental crystal creation limits. Our process consists of epitaxy, bonding, laser ejection via free space, and post-ejection finishing. In principle, our post-epitaxial surface never has to touch more than one substance, for example a gold contact. We ejected in air here but could also use an inert atmosphere or even a vacuum. The volume that forms between surfaces during ejection, at least at small displacements or in vacuum, will consist only of excited material from within the crystal. In other words, the ejection process only requires enough empty space for the crystals to separate, and then the new surfaces cool in their own, ultrapure environment. To reveal a pristine interface, we chose to remove the debris and etch stops by rapid wet etching. These layers can be etched away relatively slowly via dilute mixtures, or at 1 to 10 μm per min with standard, room temperature solutions. After removing the melt debris, another short etch reveals the final interface, e.g., the device's surface. But, for nearly all of this time, the etchant is not in contact with the device's front contact surface. No part of the front contact is exposed to the etchant until the final moments of the last etch, during the brief time that it takes to clear uneven etch fronts and verify completion before rinsing. Coupled with the variety of flow, chemistry, temperature, and orientation options available to us by avoiding thick etches and chemical undercutting, it is reasonable to expect that this process can minimize reaction product surface contamination,¹⁶ as well as surface roughness, especially for material-etchant pairs with small etch rate ratios. Hence, assuming that insoluble or otherwise persistent phases are avoidable, this process shows ideal characteristics for generalized III-V crystal splitting, bonding, and etching.

This process may also approach fundamental rate and volumetric processing limits for single-crystalline III-V optoelectronics. Our etch times were independent of device area, and we rapidly etched layers in vertical configurations. We did not peel thin films, use stressor layers, or need any of the concomitant equipment, material, or controls in our process environment. Therefore, we might minimize etch bath volumes, evaporation rates, bath contamination, cleanroom contamination, cleanroom volume, energy consumption, process interruption losses, and waste while maximizing purity, recovery, recyclability, and production rate per volume. Our results suggest pathways to, for example, square-meters-per-second throughput from an order 10 m² commercial laser station and ~0.1 m³ chemical baths, and competing across a variety of interesting fabrication metrics, such as peak watts solar per manufacturing volume per time.

It is remarkable that such a wide, thin, single-crystalline multilayer can be split with one laser pulse, via free space, without stressor layers, and with rigid (or flexible²³) boundary conditions. Characterizing and understanding the intracrystalline transients is a complex, multidisciplinary problem, especially given the breadth of this processing space. Nevertheless, it is reasonable to guess that our crystals separated along the absorber plane on order 10 ns timescales. This would be an order of magnitude faster than speed-of-sound propagation times across even the modest length scale demonstrated here. Furthermore, we are essentially optically disassembling a (quasi) single-crystal lattice everywhere at once across two dimensions. This implies that ultrafast electron-photon or electron-phonon coupling and local phase transformation times will set the ultimate speed limits for splitting crystals. Finally, because we applied selective optical absorption to split the crystal, we reserved our selective chemical etches for post-separation surface processing. We therefore demonstrated orthogonal processing operations to split crystals and expose interfaces. More generally, we can view wavelength-selective absorption as adding orthogonal dimensions to the traditional III-V processing space and postulate higher-order pathways such as optically guided epitaxial spalling, void-assisted lateral etching, or simply the rapid chemical-mechanical bifurcation of a laser-ejected GaAs|AlAs|GaAs thin film. These processing considerations, and our facile demonstration of single-crystalline minority-carrier devices, imply that crystal ejection could play an important role in the development and proliferation of critical optoelectronics.

EXPERIMENTAL PROCEDURES

Resource availability

Lead contact

Further information and requests for resources should be directed to and will be fulfilled by the lead contact, Bruce M. Clemens (bmc@stanford.edu).

Materials availability

This study did not generate any new unique materials.

Data and code availability

The data and code used during this study are available from the lead author upon reasonable request.

Device fabrication and characterization

Detailed experimental procedures are described in the [supplemental experimental procedures](#).

SUPPLEMENTAL INFORMATION

Supplemental information can be found online at <https://doi.org/10.1016/j.xcrp.2023.101449>.

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AUTHOR CONTRIBUTIONS

Conceptualization, B.A.R. and B.M.C.; investigation, B.A.R. (laser ejection, cell fabrication, X-ray diffraction [XRD], and photoluminescence [PL]), Z.Z. (TEM), M.A.S. (PV characterization, cell design, and epitaxy), and T.E.C. (evaporation); writing – original draft, B.A.R.; writing – review & editing, all authors; funding acquisition, B.A.R., M.A.S., A.M.L., and B.M.C.; resources, M.A.S. and A.M.L.; supervision, M.A.S., A.M.L., and B.M.C.

DECLARATION OF INTERESTS

B.M.C. has an active patent for crystal ejection, US patent 9,698,053 B2. The patent covers the laser removal of semiconductor thin films by wavelength-selective absorption in relatively low-band-gap, lattice-matched layers.

INCLUSION AND DIVERSITY

We support inclusive, diverse, and equitable conduct of research.

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